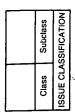


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PATENT NUMBER	

U.S. UTILITY Patent Application

SCANNED A

PATENT DATE

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z	APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER	•
	09/997589	D	4 <del>30</del> 257	724	1 <del>752</del> 2827	LUAN	THAI
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Thomas Massingill **APPLICANTS** Mark McCormack Wen-Chou Wang

Multi-chip module and method for forming and method for deplating defective capacitors  $\dot{}$ 

PTO-2040 12/99

ISSUING CLASSIFICATION CROSS REFERENCE(S) **ORIGINAL** SUBCLASS (ONE SUBCLASS PER BLOCK) SUBCLASS CLASS CLASS INTERNATIONAL CLASSIFICATION Continued on Issue Slip Inside File Jacket

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TERMINAL	DRAWINGS			CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
☐ The term of this patent		<u> </u>		NOTICE OF AL	LOWANCE MAILED	
subsequent to (date) has been disclaimed.	(Assistant Examiner)		(Date)	*		
The term of this patent shall not extend beyond the expiration date				139		
of U.S Patent. No	·			ISS	UE FEE	
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this patent have been disclaimed.	(Legal Instrum	ents Examiner)	(Date)		<u> </u>	
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orm PTO-436A Rev. 6/99)			FILED WITH:	DISK (CRF)	FICHE CD-RO	